IN THE CLAIMS:

1. (Currently amended) Apparatus, comprising:

reception circuit (33) including a frequency synthesizer;

a decoder (32) for digitally demodulating an audio file signal from said reception circuit; and

a processor (34) for re-initializing ~~initializing~~ said decoder (32) in response to a loss of a phase lock in said demodulating of said audio file signal and setting said frequency synthesizer at one of a plurality of frequencies to re-establish said phase lock in said demodulating of said audio file signal.

2. (Original) The apparatus of claim 1, wherein said plurality of frequencies comprise 900MHz range channel frequencies.

3. (Original) The apparatus of claim 2, wherein said plurality of frequencies comprises 905 MHz, 911 MHz, 917 MHz and 923 MHz.

4. (Currently amended) The apparatus of claim 1, wherein said decoder comprises an ~~eight-to-four~~ eight-to-fourteen modulation EFM digital decoder.

5. (Original) The apparatus of claim 1, wherein said demodulating said audio file signal provides a digital audio stream conforming to an I2S audio format.

6. (Original) The apparatus of claim 1, wherein said processor (34) is a microprocessor

7. (Currently amended) A computer readable ~~medium~~ storage device ~~containing~~ having software instructions recorded thereon that, when executed by a processor, performs the steps of:

receiving a modulated audio file signal;

demodulating said audio file signal to a digital audio stream;

re-initializing said demodulating in response to a loss of a phase lock in said demodulating of said audio file signal; and

setting said receiving of the modulated audio file signal at one of a plurality of channel frequencies to re-establish ~~establish~~ said phase lock in said demodulating of said audio file signal.

8. (Original) The computer readable medium of claim 7, wherein said demodulating comprises a digital eight-to-fourteen modulation EFM digital decoding of said audio file signal.

9. (Original) The computer readable medium of claim 7, wherein said plurality of frequencies comprise 905 MHz, 911 MHz, 917 MHz and 923 MHz.

10. (Original) The computer readable medium of claim 7, wherein said demodulating outputs a digital audio stream.

11. (Original) The computer readable medium of claim 7, wherein said re-initializing and setting is carried out by a processor.

12. (Withdrawn) A communications system comprising:

a remote control reception circuit (24);

a streaming controller (22) coupled to said remote control reception circuit;

an encoder (23) for converting digital audio from said controller to a modulated data signal;

a transmission circuit (25) for transmitting said modulated data signal at one of a plurality of channel frequencies selected in response to said remote control reception circuit;

reception circuit (33) including a frequency synthesizer for receiving said modulated data signal;

a demodulator coupled to said receiver for demodulating said modulated data signal; and

a processor for initializing said demodulator in response to a loss of a phase lock in said demodulating of said modulated data signal and setting said frequency synthesizer at said one of a plurality of channel frequencies until said phase lock in said demodulating is established.

13. (Withdrawn) The system of claim 12, wherein said plurality of channel frequencies comprise 900 MHz range channels.

14. (Withdrawn) The system of claim 12, wherein said plurality of channel frequencies comprise 905 MHz, 911 MHz, 917 MHz and 923 MHz.

15. (Withdrawn) The system of claim 12, wherein said modulating comprises an eight-to-fourteen modulation EFM digital encoding.

16. (Withdrawn) The system of claim 12, wherein said demodulation comprises a digital eight-to-fourteen modulation EFM digital decoding.

17. (Withdrawn) The system of claim 12, wherein said transmitter and said receiver are synchronized to said one of a plurality of channel frequencies in the 900 MHz range.

18. (Withdrawn) The system of claim 12, wherein said receiver sequences through said plurality of channel frequencies until a phase lock loop is established in a phase lock loop in said demodulating said modulated data signal.

19. (Withdrawn) An apparatus comprising:

a streaming controller (22) for providing digital audio;

an encoder (23) for converting said digital audio to a modulated data signal; and

a transmission circuit (25) for transmitting said modulated data signal at one of a plurality of channel frequencies, said transmission circuit being coupled to said encoder and said streaming controller.

20. (Withdrawn) The apparatus of claim 19, further comprising a remote control reception circuit (24) coupled to said controller.